

In the Specification:

Please replace paragraph [0030] beginning on p. 7, line 12 with the following rewritten paragraph:

-- For example, as illustrated in Figure 5, a metal-insulator-semiconductor field effect transistor (MISFET) 34 may be fabricated similarly to the embodiments in Figures 3A-3C or Figures 4A-4C by placing an insulating layer 36 between the gate contact 30 and the GaN cap layer 24. The insulation layer 36 can be any dielectric material. One example of the insulation layer 36 is an ~~in-situ pyrolytic silicon nitride passivation layer disclosed in commonly owned and assigned U.S. Patent Application No.~~ filed on ~~_____~~ and entitled ~~PASSIVATION OF GaN DEVICES IN EPITAXIAL GROWTH CHAMBER~~, which is incorporated herein by reference in its entirety. The source contact 28 and drain contact 32 are formed on the GaN cap layer 24, and the gate contact 30 is formed on the insulation layer 36. As described previously, the structural epitaxial layers 12 may be separated from the substrate 14 by oxidizing the sacrificial layer 20, and optionally the nucleation layer 18, either before or after the source contact 28, gate contact 30, and drain contact 32 are separately formed. --

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